

Electronic Acknowledgement Receipt

EFS ID:	3393573
Application Number:	10671593
International Application Number:	
Confirmation Number:	8553
Title of Invention:	DIGITAL PHASE LOCKED CIRCUIT CAPABLE OF DEALING WITH INPUT CLOCK SIGNAL PROVIDED IN BURST FASHION
First Named Inventor/Applicant Name:	Ichiro Yokokura
Customer Number:	21171
Filer:	Harry J. Staas/Renee Adams
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Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$1740
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)

1		IFandSuppDec06032008.pdf	176028 ebfb2d237053563c99ba74a5786795b2 b44454eb6	yes	3				
Multipart Description/PDF files in .zip description									
Document Description		Start		End					
Issue Fee Payment (PTO-85B)		1		1					
Oath or Declaration filed		2		3					
Warnings:									
Information:									
2	Fee Worksheet (PTO-06)	fee-info.pdf	8346 bd1c397c5d1d1584a501da5c75b8fb ff000071	no	2				
Warnings:									
Information:									
Total Files Size (in bytes):				184374					
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